

Day : Monday  
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Time: 11:32:39

## Inventor Name Search Result

Your Search was:

Last Name = HAUCK

First Name = JERROLD

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09059533</a>	<a href="#">7076716</a>	150	04/13/1998	EARLY ACKNOWLEDGMENT OF PRIMARY PACKETS	HAUCK, JERROLD
<a href="#">09102097</a>	<a href="#">6212171</a>	150	06/22/1998	METHOD AND APPARATUS FOR GAP COUNT DETERMINATION	HAUCK, JERROLD
<a href="#">09484479</a>	<a href="#">6639918</a>	150	01/18/2000	METHOD AND APPARATUS FOR BORDER NODE BEHAVIOR ON A FULL-DUPLEX BUS	HAUCK, JERROLD
<a href="#">09507120</a>	<a href="#">6831928</a>	150	02/17/2000	METHOD AND APPARATUS FOR ENSURING COMPATIBILITY ON A HIGH PERFORMANCE SERIAL BUS	HAUCK, JERROLD
<a href="#">10045191</a>	<a href="#">7023874</a>	150	10/18/2001	DISTRIBUTED ARBITRATION ON A FULL DUPLEX BUS	HAUCK, JERROLD V.
<a href="#">10269915</a>	<a href="#">6985981</a>	150	10/11/2002	METHOD AND APPARATUS FOR PREVENTING LOOPS IN A FULL-DUPLEX BUS	HAUCK, JERROLD V.
<a href="#">10390944</a>	<a href="#">6842805</a>	150	03/17/2003	METHOD AND APPARATUS FOR PREVENTING LOOPS IN A FULL-DUPLEX BUS	HAUCK, JERROLD V.
<a href="#">10613867</a>	Not Issued	61	07/02/2003	Source packet bridge	HAUCK, JERROLD V.
<a href="#">10635593</a>	Not Issued	41	08/05/2003	Method and apparatus for border node behavior on a full-duplex bus	HAUCK, JERROLD V.
<a href="#">10635706</a>	Not Issued	71	08/05/2003	Method and apparatus for border node behavior on a full-duplex bus	HAUCK, JERROLD V.
<a href="#">10635749</a>	Not Issued	93	08/05/2003	METHOD AND APPARATUS FOR BORDER NODE BEHAVIOR ON A FULL-DUPLEX BUS	HAUCK, JERROLD V.

<u>10635834</u>	<u>6891848</u>	150	08/05/2003	METHOD AND APPARATUS FOR BORDER NODE BEHAVIOR ON A FULL-DUPLEX BUS	HAUCK, JERROLD V.
<u>10635835</u>	Not Issued	98	08/05/2003	METHOD AND APPARATUS FOR BORDER NODE BEHAVIOR ON A FULL-DUPLEX BUS	HAUCK, JERROLD V.
<u>10635836</u>	Not Issued	93	08/05/2003	METHOD AND APPARATUS FOR BORDER NODE BEHAVIOR ON A FULL-DUPLEX BUS	HAUCK, JERROLD V.
<u>10728185</u>	Not Issued	30	12/03/2003	Fly-by and ack-accelerated arbitration for broadcast packets	HAUCK, JERROLD V.
<u>10741782</u>	Not Issued	161	12/19/2003	Method and apparatus for border node behavior on a full-duplex bus	HAUCK, JERROLD V.
<u>10984535</u>	<u>7050453</u>	150	11/08/2004	METHOD AND APPARATUS FOR ENSURING COMPATIBILITY ON A HIGH PERFORMANCE SERIAL BUS	HAUCK, JERROLD V.
<u>11021337</u>	<u>7194564</u>	150	12/21/2004	METHOD AND APPARATUS FOR PREVENTING LOOPS IN A FULL-DUPLEX BUS	HAUCK, JERROLD V.
<u>11284674</u>	Not Issued	30	11/21/2005	Method and apparatus for ensuring compatibility on a high performance serial bus	HAUCK, JERROLD V.
<u>11601487</u>	Not Issued	41	11/16/2006	Method and apparatus for border node behavior on a full-duplex bus	HAUCK, JERROLD V.
<u>11725711</u>	Not Issued	19	03/19/2007	Method and apparatus for loop breaking in a data bus	HAUCK, JERROLD V.
<u>60523525</u>	Not Issued	159	11/18/2003	Symbol encoding for tolerance to single byte errors	HAUCK, JERROLD V.
<u>08419005</u>	Not Issued	166	04/07/1995	INTEGRATING DATA SCALING AND BUFFERING FUNCTIONS TO MINIMIZE MEMORY REQUIREMENT	HAUCK, JERROLD V.
<u>08623759</u>	<u>5809337</u>	150	03/29/1996	MASS STORAGE DEVICES UTILIZING HIGH SPEED SERIAL COMMUNICATIONS	HAUCK, JERROLD V.
<u>08626221</u>	Not Issued	166	03/29/1996	COMPUTER SYSTEM SECURITY	HAUCK, JERROLD V.
<u>08721269</u>	<u>5768544</u>	150	09/26/1996	DETERMINISTIC LATENCY CHARACTERIZATION AND	HAUCK, JERROLD V.

				MITIGATION	
<u>08887415</u>	<u>6091426</u>	150	07/03/1997	INTEGRATING DATA SCALING AND BUFFERING FUNCTIONS TO MINIMIZE MEMORY REQUIREMENT	HAUCK, JERROLD V.
<u>08959101</u>	<u>6009527</u>	150	10/23/1997	COMPUTER SYSTEM SECURITY	HAUCK, JERROLD V.
<u>08977460</u>	<u>6145018</u>	150	11/24/1997	METHOD FOR HINDERING SOME TYPES OF NODES FROM BECOMING A BUS ARBITRATION CONTROLLER	HAUCK, JERROLD V.
<u>09017425</u>	<u>6356558</u>	150	02/02/1998	IMPROVED ARBITRATION TECHNIQUES FOR A HIGH SPEED SERIAL BUS	HAUCK, JERROLD V.
<u>09017451</u>	<u>6411628</u>	150	02/02/1998	DISTRIBUTED ARBITRATION ON A FULL DUPLEX BUS	HAUCK, JERROLD V.
<u>09018028</u>	<u>6038234</u>	150	02/02/1998	EARLY ARBITRATION ON A FULL DUPLEX BUS	HAUCK, JERROLD V.
<u>09416497</u>	<u>6765923</u>	150	10/12/1999	EARLY ARBITRATION ON A FULL DUPLEX BUS	HAUCK, JERROLD V.
<u>09435160</u>	<u>6587904</u>	150	11/05/1999	METHOD AND APPARATUS FOR PREVENTING LOOPS IN A FULL-DUPLEX BUS	HAUCK, JERROLD V.
<u>10464246</u>	<u>6977887</u>	150	06/17/2003	METHOD AND APPARATUS FOR LOOP BREAKING ON A SERIAL BUS	HAUCK, JERROLD VON
<u>10735260</u>	Not Issued	71	12/11/2003	Symbol encoding for tolerance to single byte errors	HAUCK, JERROLD VON
<u>10749791</u>	Not Issued	71	12/29/2003	Gap count analysis for the p1394a bus	HAUCK, JERROLD VON
<u>10750325</u>	Not Issued	30	12/30/2003	Hardware assist for 61883 pacing	HAUCK, JERROLD VON
<u>11141944</u>	Not Issued	30	05/31/2005	Method and apparatus for loop breaking on a serial bus	HAUCK, JERROLD VON
<u>09350583</u>	<u>6628607</u>	150	07/09/1999	METHOD AND APPARATUS FOR LOOP BREAKING ON A SERIAL BUS	HAUCK, JERROLD VON

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## Inventor Name Search Result

Your Search was:

Last Name = DUCKWALL

First Name = WILLIAM

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09121616</a>	<a href="#">6266334</a>	150	07/20/1998	METHOD FOR OPTIMIZING ACKNOWLEDGE PACKET RATE	DUCKWALL, WILLIAM S
<a href="#">09751696</a>	<a href="#">6314461</a>	150	12/28/2000	Method and apparatus for the addition and removal of nodes from a common interconnect	DUCKWALL, WILLIAM S.
<a href="#">09752646</a>	<a href="#">6954787</a>	150	12/29/2000	METHOD AND APPARATUS FOR THE ADDITION AND REMOVAL OF NODES FROM A COMMON INTERCONNECT	DUCKWALL, WILLIAM S.
<a href="#">10059556</a>	<a href="#">6763414</a>	150	01/28/2002	FLY-BY SERIAL BUS ARBITRATION	DUCKWALL, WILLIAM S.
<a href="#">10136168</a>	<a href="#">6711173</a>	150	04/30/2002	FLY-BY SERIAL BUS ARBITRATION	DUCKWALL, WILLIAM S.
<a href="#">10186788</a>	<a href="#">6904044</a>	150	06/28/2002	FLY-BY SERIAL BUS ARBITRATION	DUCKWALL, WILLIAM S.
<a href="#">10214285</a>	<a href="#">6839791</a>	150	08/05/2002	METHOD AND APPARATUS FOR ACCELERATING DETECTION OF SERIAL BUS DEVICE SPEED SIGNALS	DUCKWALL, WILLIAM S.
<a href="#">10231802</a>	<a href="#">6721330</a>	150	08/30/2002	FLY-BY SERIAL BUS ARBITRATION	DUCKWALL, WILLIAM S.
<a href="#">10728185</a>	Not Issued	30	12/03/2003	Fly-by and ack-accelerated arbitration for broadcast packets	DUCKWALL, WILLIAM S.
<a href="#">10884096</a>	Not Issued	30	07/02/2004	Fly-by serial bus arbitration	DUCKWALL, WILLIAM S.
<a href="#">11021338</a>	<a href="#">7096302</a>	150	12/21/2004	METHOD AND APPARATUS FOR ACCELERATING DETECTION OF SERIAL BUS DEVICE SPEED SIGNALS	DUCKWALL, WILLIAM S.
<a href="#">11184379</a>	Not Issued	71	07/18/2005	Method and apparatus for the addition and removal of nodes	DUCKWALL, WILLIAM S.

				from a common interconnect	
<u>08316552</u>	<u>5495481</u>	150	09/30/1994	METHOD AND APPARATUS FOR ACCELERATING ARBITRATION IN A SERIAL BUS BY DETECTION OF ACKNOWLEDGE PACKETS	DUCKWALL, WILLIAM S.
<u>08359294</u>	Not Issued	166	12/19/1994	METHOD AND APPARATUS FOR THE ADDITION AND REMOVAL OF NODES FROM A COMMON INTERCONNECT	DUCKWALL, WILLIAM S.
<u>08478412</u>	Not Issued	166	06/07/1995	METHOD AND APPARATUS FOR ACCELERATING ARBITRATION IN A SERIAL BUS BY DETECTION OF ACKNOWLEDGE PACKETS	DUCKWALL, WILLIAM S.
<u>08565690</u>	<u>5802057</u>	150	12/01/1995	FLY-BY SERIAL BUS ARBITRATION	DUCKWALL, WILLIAM S.
<u>08565986</u>	<u>5784648</u>	150	12/01/1995	TOKEN STYLE ARBITRATION ON A SERIAL BUS BY PASSING AN UNREQUESTED BUS GRAND SIGNAL AND RETURNING THE TOKEN BY A TOKEN REFUSAL SIGNAL	DUCKWALL, WILLIAM S.
<u>08691301</u>	<u>5802048</u>	150	08/01/1996	METHOD AND APPARATUS FOR ACCELERATING ARBITRATION IN A SERIAL BUS BY DETECTION OF ACKNOWLEDGE PACKETS	DUCKWALL, WILLIAM S.
<u>08889814</u>	<u>5875301</u>	150	07/08/1997	METHOD AND APPARATUS FOR THE ADDITION AND REMOVAL OF NODES FROM A COMMON INTERCONNECT	DUCKWALL, WILLIAM S.
<u>09143422</u>	<u>6385679</u>	150	08/28/1998	FLY-BY SERIAL BUS ARBITRATION	DUCKWALL, WILLIAM S.
<u>09186921</u>	<u>5935208</u>	150	11/06/1998	INCREMENTAL BUS RECONFIGURATION WITHOUT BUS RESETS	DUCKWALL, WILLIAM S.
<u>09330896</u>	<u>6199119</u>	150	06/11/1999	METHOD AND APPARATUS FOR THE ADDITION AND REMOVAL OF NODES FROM A COMMON INTERCONNECT	DUCKWALL, WILLIAM S.
<u>09441390</u>	<u>6457086</u>	150	11/16/1999	A METHOD AND APPARATUS FOR ACCELERATING	DUCKWALL, WILLIAM S.

				DETECTION OF SERIAL BUS DEVICE SPEED SIGNALS	
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**Inventor Name Search Result**

Your Search was:

Last Name = KANHERE

First Name = PRASHANT

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">10728185</a>	Not Issued	30	12/03/2003	Fly-by and ack-accelerated arbitration for broadcast packets	KANHERE, PRASHANT
<a href="#">06924797</a>	<a href="#">4823312</a>	150	10/30/1986	ASYNCHRONOUS COMMUNICATIONS ELEMENT	KANHERE, PRASHANT A.
<a href="#">07295610</a>	Not Issued	166	01/10/1989	PROGRAMMABLE RECEIVER FIFO TRIGGER LEVEL IN AN ASYNCHRONOUS COMMUNICATIONS ELEMENT	KANHERE, PRASHANT A.
<a href="#">07462841</a>	Not Issued	166	12/22/1989	PROGRAMMABLE RECEIVER FIFO TRIGGER LEVEL IN AN ASYNCHRONOUS COMMUNICATIONS ELEMENT	KANHERE, PRASHANT A.
<a href="#">07703572</a>	<a href="#">5241660</a>	150	05/17/1991	BUFFERED ASYNCHRONOUS COMMUNICATIONS ELEMENT WITH RECEIVE/TRANSMIT CONTROL AND STATUS REPORTING	KANHERE, PRASHANT A.
<a href="#">08038713</a>	<a href="#">5287458</a>	150	03/26/1993	BUFFERED ASYNCHRONOUS COMMUNICATIONS ELEMENTS WITH RECEIVE/TRANSMIT CONTROL AND STATUS REPORTING	KANHERE, PRASHANT A.

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<input type="button" value="Search"/>	

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